

HA-2620-1

High Temperature, Wide Band, High Impedance Operational Amplifier

JUNE 1980

FEATURES	DESCRIPTION
CHARACTERIZED TO 250°C GUARANTEED FOR OPERATION AT 200°C WIDE BANDWIDTH HIGH INPUT IMPEDANCE LOW INPUT BIAS CURRENT LOW INPUT OFFSET CURRENT TOWN INPUT OFFSET VOLTAGE HIGH GAIN APPLICATIONS OIL WELL-LOGGING GEOTHERMAL WELL-LOGGING INDUSTRIAL PROCESS CONTROL ENGINE TESTING AND CONTROL	The HA-2620 is an uncompensated bipolar operational amplifier that features very high input inpedance (500 M) coupled with wideband AC performance. The high resistance of the input stage is complemented by low offset voltage. (6mV) and low bias and offset currents (30nA) at 200°C to facilitate accurate signal processing. High gain bandwidth product, slew rate, and open loop gain allows the 2620 to perform high-gain amplification of fast wideband signals. With the HA-2620-1 configuration Harris offers a device that has been characterized up to 250°C and life-tested at 200°C. A final electrical test at 200°C is also included in the process flow. This amplifier is ideal for high temperature applications such as oil and geothermal well-logging, industrial process control, engine control, etc.
PINOUT	SCHEMATIC
DFFSET ADJ. OFFSET ADJ. OFFSET ADJ. OFFSET ADJ. OFFSET ADJ.	OFFSET R1 R2 R3 R4 1.56K 1.56K 0FFSET 028 039 041 041 055 058 056 R18 013 013 013 013 013 013 013 013 013 013

CASE CONNECTED TO V-

ABSOLUTE MAXIMUM RATINGS

Voltage Between V+ and V- Terminals Differential Input Voltage Peak Output Current Internal Power Dissipation Operating Temperature Range Storage Temperature Range Maximum Junction Temperature $\begin{array}{l} 45.0\,\text{V} \\ \pm \ 12.0\,\text{V} \\ \text{Full Short Circuit Protection} \\ 145\text{mW at } 200^{^{\text{O}}\text{C}} \\ 0^{^{\text{O}}\text{C}} \leq \text{T}_{\text{A}} \leq +200^{^{\text{O}}\text{C}} \\ -65^{^{\text{O}}\text{C}} \leq \text{T}_{\text{A}} \leq +200^{^{\text{O}}\text{C}} \\ 220^{^{\text{O}}\text{C}} \end{array}$

ELECTRICAL CHARACTERISTICS

 $V_{+} = +15V DC, V_{-} = -15V DC$

HA-2620-1
Specifications at T = +200°C
unless otherwise noted

PARAMETER	MIN	TYP	MAX	UNITS
INPUT CHARACTERISTICS				
Offset Voltage		2	6	mV
Offset Voltage Average Drift		5		$\mu V/^{0}C$
Bias Current		10	35	nA
Offset Current		5	35	nA
Input Resistance (T = +25°C)	100	500		${\sf m}\Omega$
Common Mode Range	<u>+</u> 11.0			V
TRANSFER CHARACTERISTICS				
Large Signal Voltage Gain (Notes 1, 4)	50K			V/V
Common Mode Rejection Ratio (Note 2)	80	100		dB
Gain Bandwidth Product (Notes 1, 3, 6) (T = +25°C)		100		MHz
OUTPUT CHARACTERISTICS				
Output Voltage Swing (Note 1)	<u>+</u> 10.0	<u>+</u> 12.0		V
Output Current (Note 4) (T = +25°C)	<u>+</u> 15	<u>+</u> 22		mA
Full Power Bandwidth (Note 4) (T = +25°C)	400	600		kHz
POWER SUPPLY CHARACTERISTICS				
Supply Current		3.0	4.0	mA
Power Supply Rejection Ratio (Note 5)	80	90		dB

Notes:

1.
$$R_L = 2 k$$
, $C_L = 50 pF$

3.
$$V_0 < 90 \text{mV}$$

5.
$$V_S = \pm 9.0V \text{ to } \pm 15V$$

2.
$$V_{cm} = \pm 10.0V$$

4.
$$V_0 = \pm 10V$$

V+ = 15V DC, V- = -15V DC, T_A = +200 $^{\rm o}$ C UNLESS OTHERWISE STATED

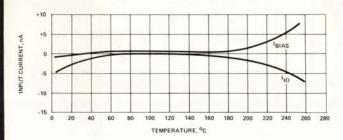


FIGURE 1. BIAS AND OFFSET CURRENT VERSUS TEMPERATURE

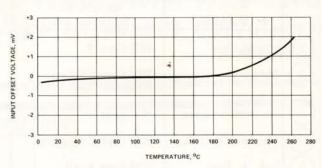


FIGURE 2. INPUT OFFSET VOLTAGE VERSUS TEMPERATURE

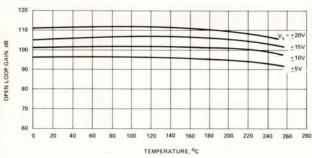


FIGURE 3. OPEN LOOP GAIN VERSUS TEMPERATURE

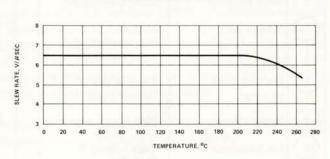


FIGURE 4. SLEW RATE VERSUS TEMPERATURE

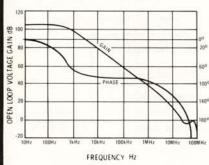


FIGURE 5. OPEN LOOP
FREQUENCY AND PHASE RESPONSE

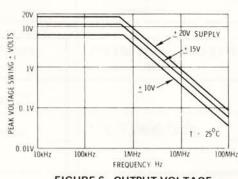
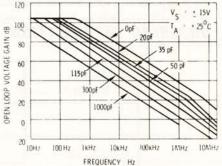


FIGURE 6. OUTPUT VOLTAGE SWING VERSUS FREQUENCY



Note External Compensation is Required For Closed Loop Gain < 5 If External Compensation is Used, Also Connect 100 pF Capacitor From Output to Ground.

FIGURE 7. OPEN-LOOP FREQUENCY RESPONSE FOR VARIOUS VALUES OF CAPACITORS FROM BANDWIDTH CONTROL PIN TO GROUND

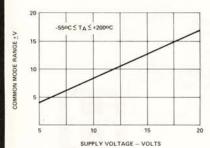


FIGURE 8. COMMON MODE VOLTAGE RANGE AS A FUNCTION OF SUPPLY VOLTAGE

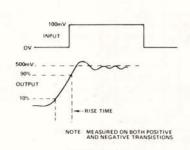


FIGURE 9. TRANSIENT RESPONSE

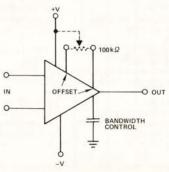


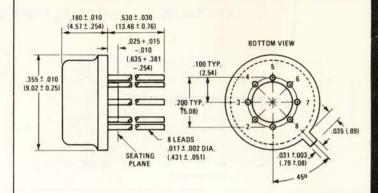
FIGURE 10. SUGGESTED OFFSET ZERO ADJUST HOOKUP

DASH 1 PRODUCT FLOW

HARRIS SEMICONDUCTOR DASH 1 PRODUCT FLOW 100% SCREENING PROCEDURE

	SCREEN	MIL-STD-883 METHOD/COND
1	Internal Visual	2010 Cond. B
2	Stabilization Bake	1008 Cond. C (24 hrs. minimum)
3	Temperature Cycling	1010 Cond. C
4	Constant Acceleration	2001 Cond. E; Y1 plane
(5)	Seal: A Fine	1014 Cond. A or B
Υ	B Gross	1014 Cond. C
6	Initial Electrical	Harris Specifications
7	Burn-In Test	1015, 160 hrs. @ 125 ⁰ C (or equivalent) (Burn-Ir circuits enclosed)
(3)	Electrical 100% go-no-go	Tested at Worst Case Operating Conditions
9	External Visual	2009 Sample Inspection
10	Lot Acceptance	Table I, Group A Elect. Tests
11)	Final Electrical	Harris Specifications @ +200 ⁰ C

PACKAGE



- 1. All dimensions in inches; millimeters are shown in parentheses.
- 2. All dimensions +.010 (+0.25mm) unless otherwise shown.

RELIABILITY INFORMATION

Life test data for HA2620-1 is available in Reliability Bulletin 137

ORDERING INFORMATION

MODEL NUMBER	OPERATING TEMPERATURE RANGE	PRODUCT DESCRIPTION
		HI-Rel Integrated Circuit
HA2-2620-1 0°C to +200°C	Characterized and tested at 200°C	

SALES OFFICES

1503 SO. COAST DRIVE SUITE 320 COSTA MESA, CA. 92626 (714) 540-2176

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